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EXIBIT A: DESCRIPTION OF THE SOFTWARE

A. p-VEX VHDL description

- This is the VHDL description of the p-VEX VLIW processor. It contains the files necessary to simulate and synthesize the design.

B. p-VEX manual

- The manual contains an introduction in how to setup a development environment for p-VEX and how to compile and run software for p-VEX. It also explains how to synthesize the design for a Xilinx virtex 6 FPGA.